

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Dojun Rhee

Serial No. :

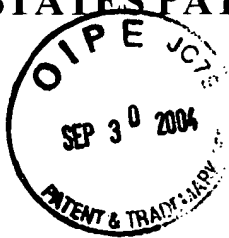
09/994,556

Filed :

November 27, 2001

For :

Unequal Error Protection Reed-Muller Code Generator and Decoder



Group Art Unit :

2133

Examiner :

Baker, S.

Atty Docket :

4028-00300 / 99-269

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Manu Kashyap

9/30/04

Date

Manu Kashyap

Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Date: 9-30-04

Respectfully submitted,

Sandeep Jaggi

Sandeep Jaggi

Reg. No. 43,331